



Electrical Characteristics ($T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol Min. Max. Unit Test Conditions

Off Characteristics

$V_{(Min)}$

[Redacted]

[Redacted]

[Redacted]

[Redacted]

Test Circuit



Figure 1: Gate Charge Test Circuit & Waveform

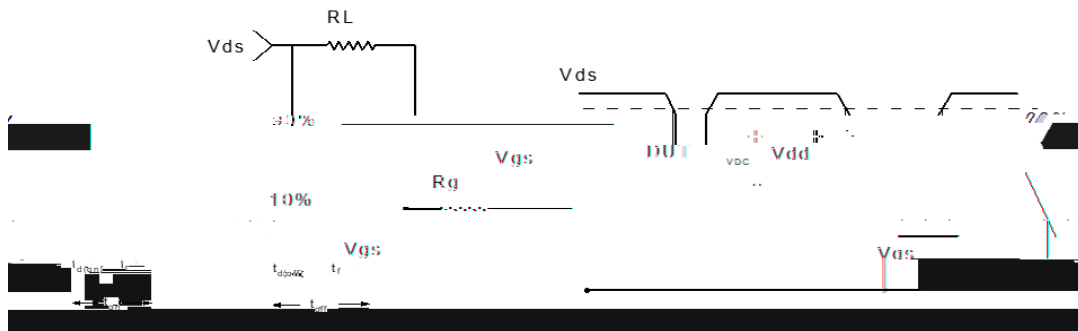


Figure 2: Resistive Switching Test Circuit & Waveform

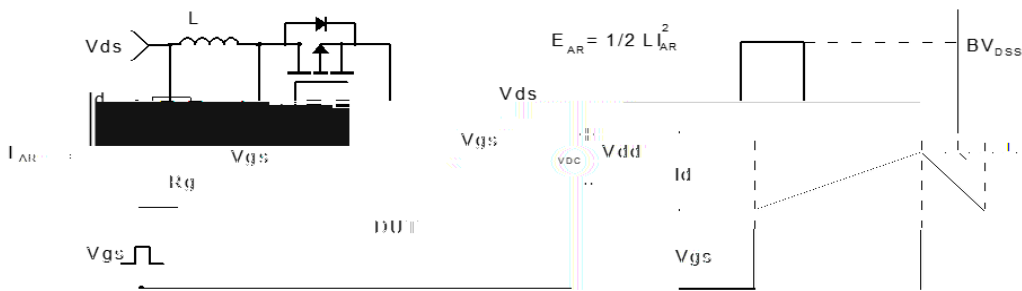


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform



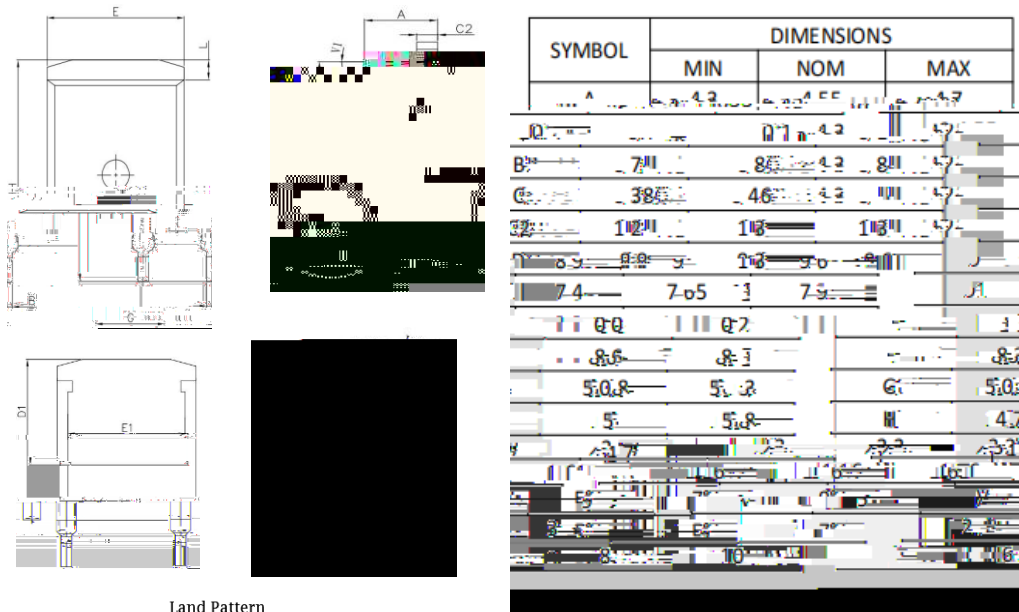
Figure 4: Diode Recovery Test Circuit & Waveform



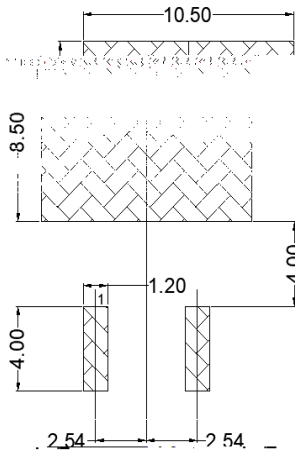


Package Mechanical Data(TO-263 -3L)

Package Outline



Land Pattern
(Only for Reference)



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co.,Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement. Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information.

 is a registered trademark of Jiangsu JieJie Microelectronics Co.,Ltd.

